CSE260 – Introduction to Digital Logic

- Minterms
- Simplification
- Timing Diagrams

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Review Problems

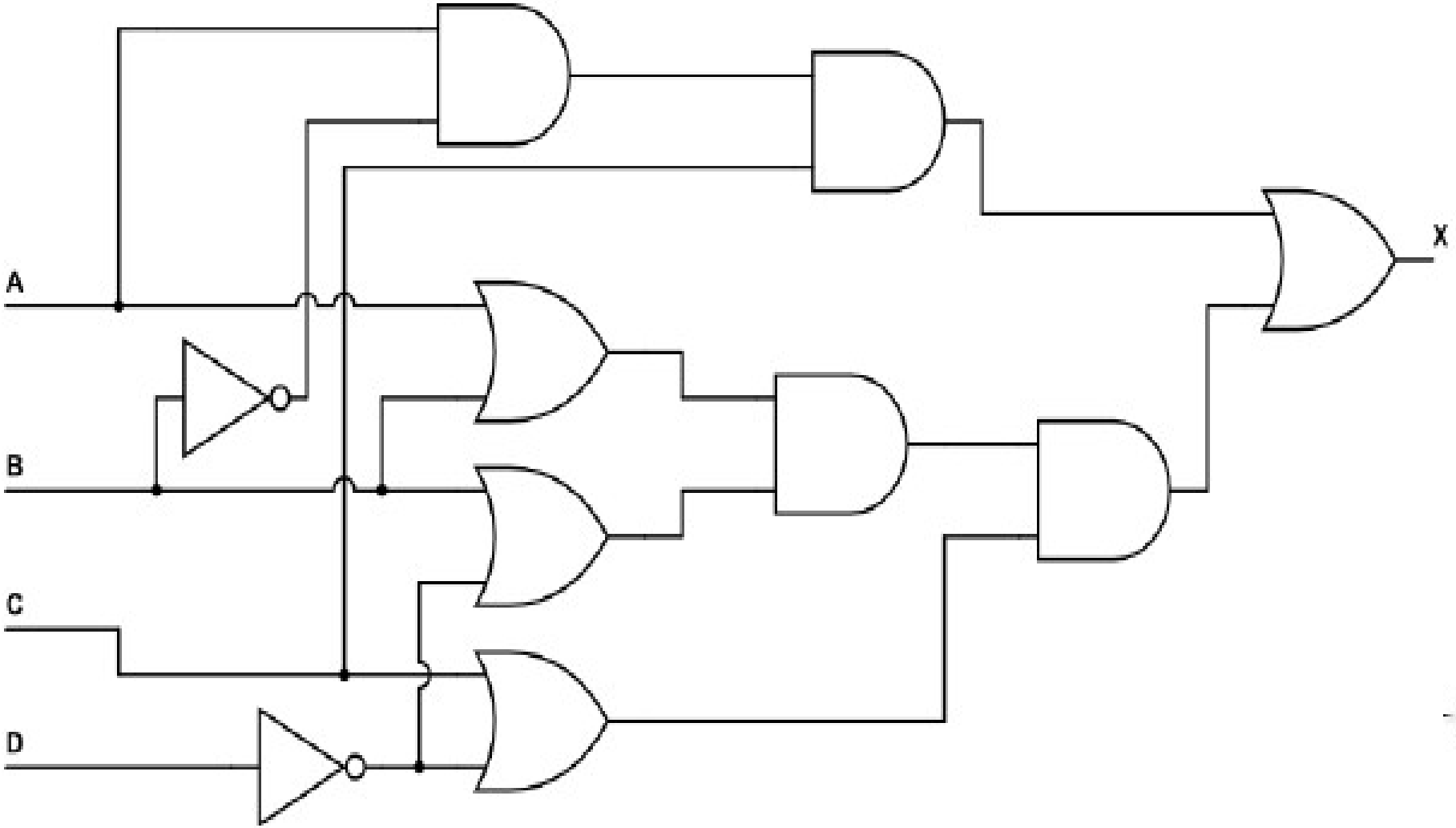
Given the following boolean expression:

(A'C + B'C + A)D + AC'

- Rewrite this expression in sum of products form
- Rewrite this expression in sum of minterms form
- Simplify this expression. Use a truth table to verify your answer.

Review Problems

- Construct a logic equation from the following circuit:



Review Problems

- Simplify the equation for the circuit on the previous slide. Use a truth table to verify your answer.
- Construct a timing diagram for this circuit using the following inputs:

ABCD A'BC'D A'B'C'D' ABC'D'

Review Problems

- You are given the following truth table:

ABC	X
000	0
001	1
010	0
011	0
100	0
101	1
110	1
111	1

- Construct a logic equation from this truth table
- Simplify the equation. Verify that it is correct with a truth table.