Introduction to Digital Logic

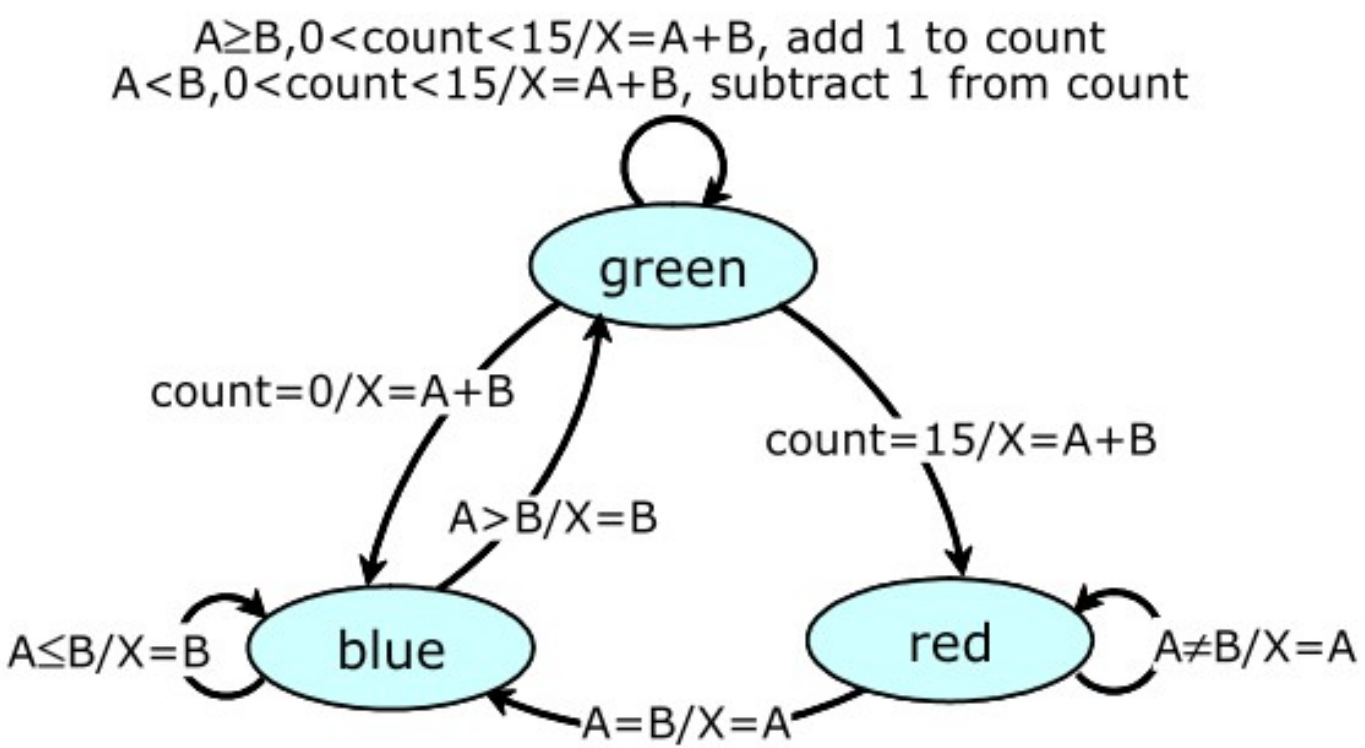
- HLSM Design

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Lab 5

Review Questions

- Construct a data path, transition table, and VHDL code for the following HLSM. The start state is green, and the initial value of count is 8.



Review Questions

- Construct a HLSM for a circuit that detects the number of times a value X becomes larger than a value Y. X and Y are both 4 bit inputs. The output is an 8-bit value that represents the number of times X was less than Y then became greater than Y.
- Construct a datapath
- Construct a transition table
- Write the VHDL