

CSE464 Tentative Schedule Spring 2005

Subject to Refinement and Adjustment

M-W 4-5:30 Cupples II, Room 200

Lecture #	Date	Topic	Read by Lecture
			(skim)
		Intro, Logistics, Grading, Motivation, Ten Commandments (Plus) (L0, L1)	Preface Chapt 1
1	19-Jan		
2	24-Jan	Packaging; noise margin	Chapt 2
3	26-Jan	Noise margin; HSPICE	Notes
4	31-Jan	Wires (L2)	Chapt 3
5	2-Feb	Feedthrough, More Wires (L3)	
6	7-Feb	Noise (L4)	
7	9-Feb	Bus connections, even-odd mode	
8	14-Feb	Crosstalk (L5)	Chapt 6
9	16-Feb	Bergeron Method (Nonlinear components)	
10	21-Feb	Signalling Intro (L6)	
11	23-Feb	More Signalling (L7)	Chapt 7
12	28-Feb	Advanced Signalling (L8)	Chapt 8
13	2-Mar	Exam I	
	7-Mar	Spring Break	
	9-Mar	Spring Break	
14	14-Mar	Advanced Signalling Continued	
15	16-Mar	OffChipPower (L9)	Chapt 5
16	21-Mar	Timing (L10)	Chapt 9
17	23-Mar	Closed-Loop Timing (L11)	
18	28-Mar	Clock Distribution (L12)	
19	30-Mar	Synchronization (L13)	Chapt 10
20	4-Apr	Synchronization Continued	
21	6-Apr	Synchronization Continued	
22	11-Apr	Synchronizer design (L14)	
23	13-Apr	Synchronizer design continued	
24	18-Apr	Catchup	
25	20-Apr	Catchup, Review of Exam II Material	
	25-Apr	Exam II	
26	27-Apr	Catchup	
27	2-May	Last Day of Class	
	5-May	Final Exams Begin	
	9-May	Final exam 4:00 - 6:00 PM, Rebstock 215	
	11-May	Finals End	
	20-May	Commencement	